
'Vivado Design Suite User Guide Xilinx

May 2nd, 2018 - Vivado Design Suite User Guide Logic Simulation UG900 v2017 3 October 4 2017 UG900 v2017 4 December 20 2017'

'Intel Quartus Prime Standard Edition Handbook Volume 3

May 3rd, 2018 - This document describes simulating designs that target Intel FPGA devices Simulation verifies design behavior before device programming The Intel ® Quartus ® Prime software supports RTL and gate level design simulation in supported EDA simulators'

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