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# The X86 Microprocessor

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*May 11th, 2018 - Some of the most important  
and most frequently used instructions are  
those that move data Without them there would  
be no way for registers or memory to even have  
anything in them to operate on'*

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*December 24th, 2016 - Intel s groundbreaking  
8008 microprocessor was first produced 45  
years ago 1 This chip Intel s first 8 bit  
microprocessor is the ancestor of the x86  
processor family that you may be using right  
now I couldn t find good die photos of the*

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8008 so I opened one up and took some detailed photographs'

**'Finding a String Length in Assembly Language**

May 9th, 2018 - by G Adam Stanislaw Whiz Kid Technomagic The Unix programmer often works with C style text strings which consist of the text followed by a NUL i e a byte equal to 0'

**'EMU8086 ASSEMBLER AND EMULATOR**

May 11th, 2018 - 8086 assembler and microprocessor emulator Study computer architecture and assembly language programming Emulate hardware screen memory and input output devices'

**'CPU World Microprocessors CPUs**

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January 18th, 2018 - The new year has indeed started out with a bang for the computer industry Two highly publicized security flaws in the Intel x86 chip architecture have now emerged They appear to affect other microprocessors made by AMD and designs licensed by ARM And they may be some of the worst computer bugs in' **'Intel 80486 Wikipedia**

May 7th, 2018 - The Intel 80486 also known as the i486 or 486 is a higher performance follow up to the Intel 80386 microprocessor The 80486 was introduced in 1989 and was the first tightly pipelined x86 design as well as the first x86 chip to use more than a million transistors due to a large on chip cache and an integrated floating point unit'

**'x86 Wikipedia**

May 6th, 2018 - x86 is a family of backward compatible instruction set architectures based on the Intel 8086 CPU and its Intel 8088 variant The 8086 was introduced in 1978 as a fully 16 bit extension of Intel s 8 bit based 8080 microprocessor with memory segmentation as a solution for addressing more memory than can be covered by a plain 16 bit address'

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